**ANSWERS**:

Both AND and OR gates have a delay of **1 nano second**. The 4-bit Full Adder had a delay of **3 nano seconds**. The RCA and CSA both also had a delay of **3 nano seconds**.

This is what we expected since a one bit Full Adder has to go through a minimum of 3 gates. With each gate having 1 nano second (regardless of whether it is an AND gate or an OR gate) it would sum up to 3 nano seconds.

All other adders having a delay of 3 nano seconds makes sense since they are just comprised of multiple FAs so the delay of a single FA would be the delay of the entire adder.

**Timing Descriptions:**

Both AND and OR gates have a delay of **1 nano second**. The 4-bit Full Adder had a delay of **3 nano seconds**. The RCA and CSA both also had a delay of **3 nano seconds**.

All succeeding adders after the 1 bit FA had a delay of 3 nano seconds.